

1/5

Fig. 1

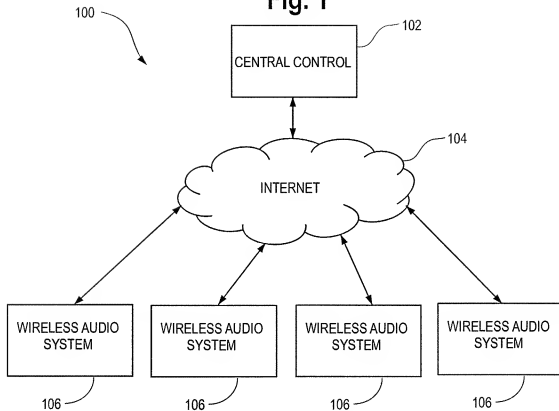


Fig. 2

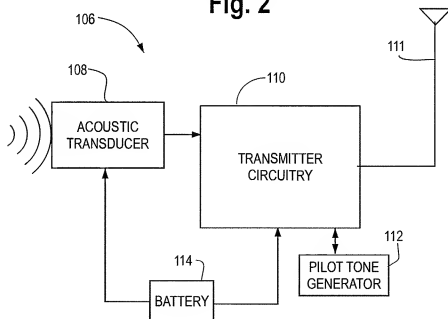


Fig. 3A

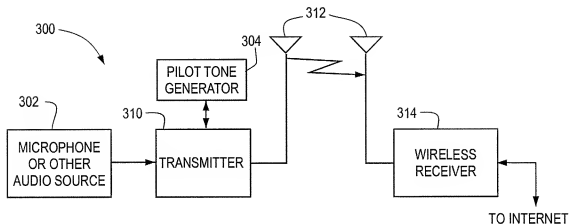
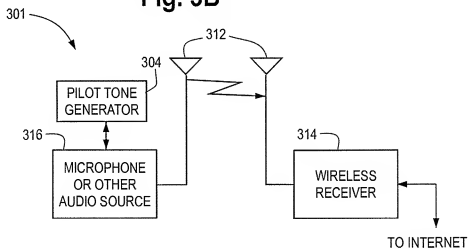


Fig. 3B



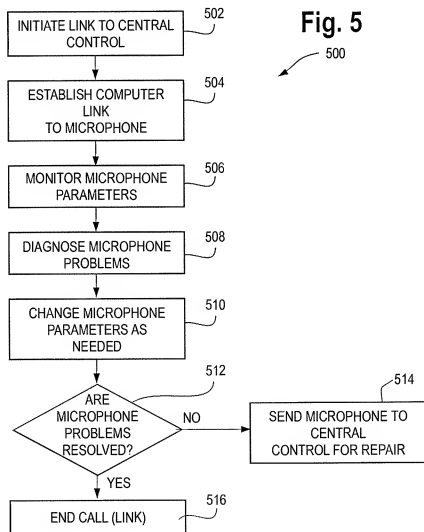
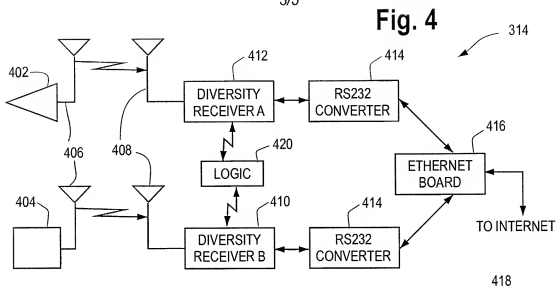


Fig. 6

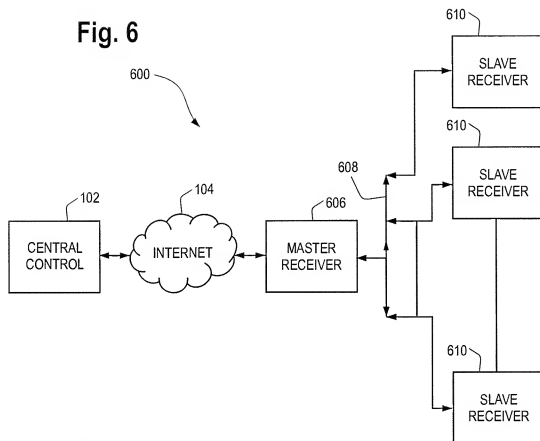


Fig. 7

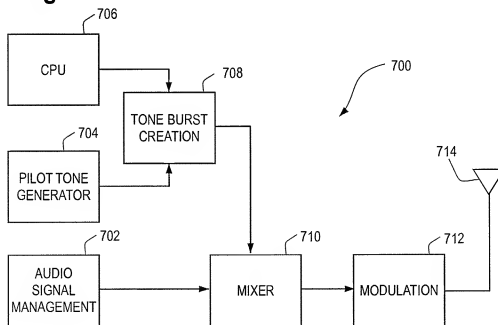


Fig. 8

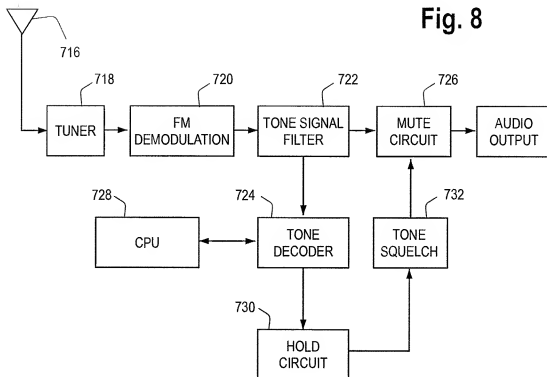


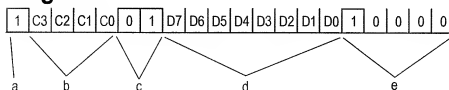
Fig. 9A



Fig. 9B



Fig. 9C



- a. START BIT (1 BIT)
- b. INDEX (4 BIT)
- c. SEPARATOR (2 BIT)
- d. DATA (8 BIT)
- e. STOP SEQUENCE (5 BIT)